EAST - [default.wsp:1]

File View Edit Tools Window Help

Active
- L1: (140473) (pll (phase near8 loop))
- L2: (126818) (phase near8 (comparator compar$4))
- L3: (37116) 1 and 2
- L4: (13248) 3 and (phase near8 shift$3)
- L5: (159560) (width near8 (determin$4 detect$3))
- L6: (1212) 4 and 5
- L7: (713) 369/47.28
- L8: (24827) G11B005/09
- L9: (1928) 331/16
- L10: (3802) 331/17
- L11: (10324) H03L007/00
- L13: (8753) H03L007/06
- L12: (1573) 327/159
- L14: (16) 327/159
- L15: (46) 331/16
- L16: (100) 331/17
- L17: (48) 369/47.28

Failed

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DBs: USPAT | Plurals | Highlight all hit terms initially

Default operator: OR

for 10/824264

BRS form | IS&R form | Image | Text | HTML

	Type	L #	Hits	Search Text	DBs	Time Stam	Comment	Error Definit	Er
1	BRS	L1	140473	(pll (phase near8 loop))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM TDB	2007/11/16 15:32			
2	BRS	L2	126818	(phase near8 (comparator compar$4)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM TDB	2007/11/16 15:32			
3	BRS	L3	37116	1 and 2	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM TDB	2007/11/16 15:32			
4	BRS	L4	13248	3 and (phase near8 shift$3)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM TDB	2007/11/16 15:33			
5	BRS	L5	159560	(width near8 (determin$4 detect$3)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM TDB	2007/11/16 15:33			
6	BRS	L6	1212	4 and 5	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM TDB	2007/11/16 16:07			
7	BRS	L7	713	369/47.28	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM TDB	2007/11/16 16:07			
8	BRS	L8	24827	G11B005/09	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM TDB	2007/11/16 16:07			
9	BRS	L9	1928	331/16	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM TDB	2007/11/16 16:07			
10	BRS	L10	3802	331/17	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM TDB	2007/11/16 16:07			
11	BRS	L11	10324	H03L007/00	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM TDB	2007/11/16 16:08			
12	BRS	L13	8753	H03L007/06	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM TDB	2007/11/16 16:08			

Hits | Details | HTML

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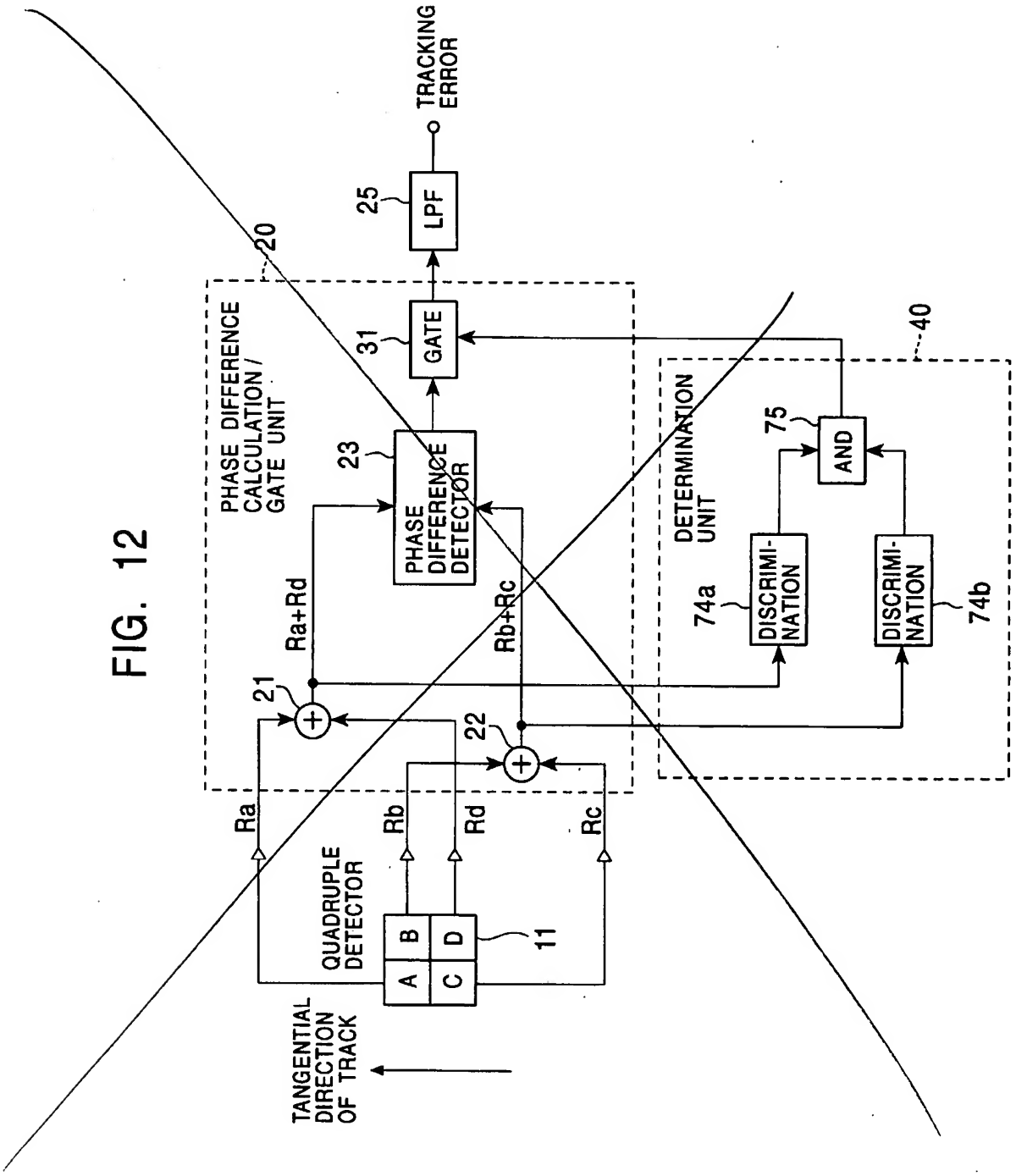
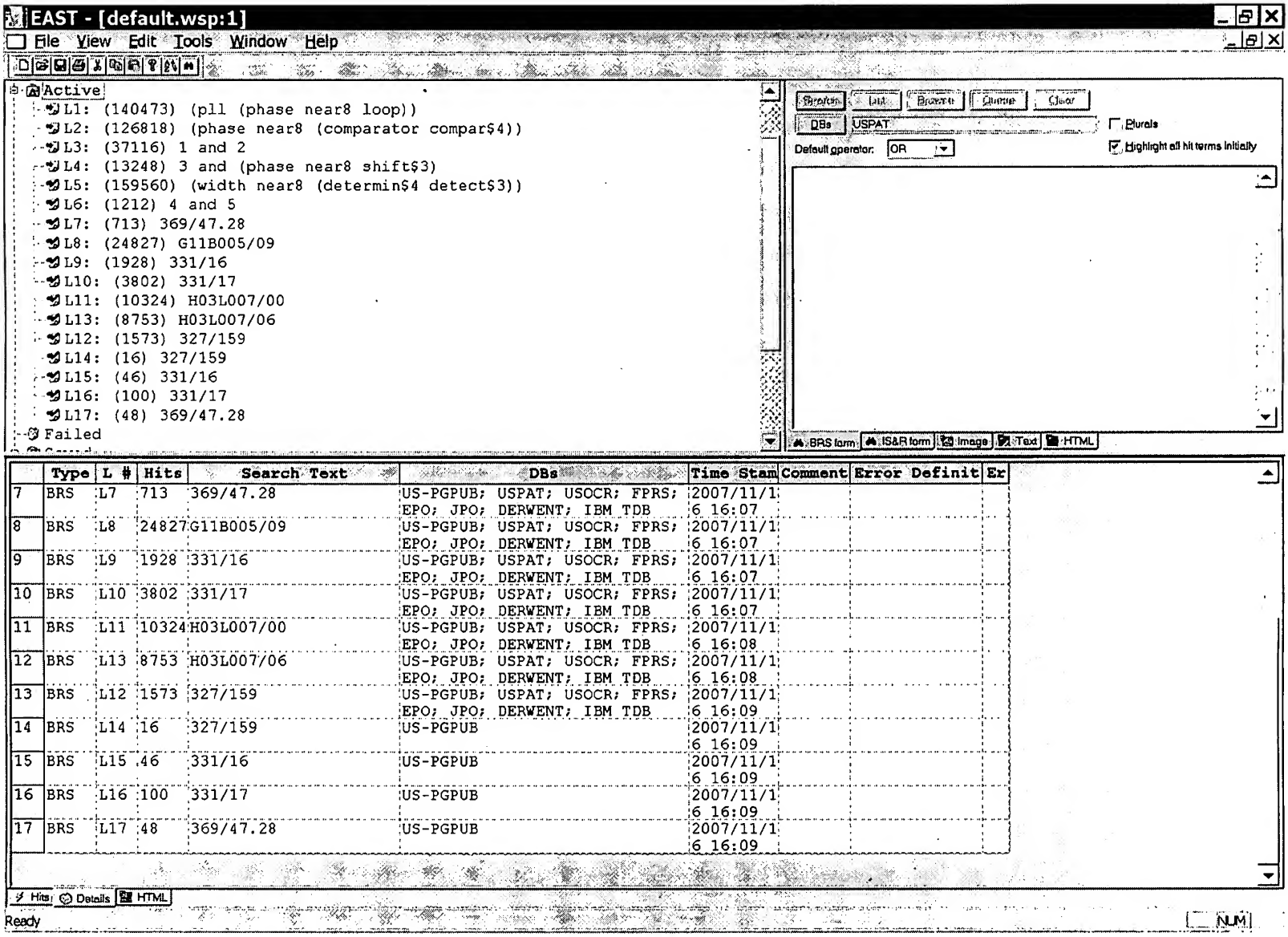
FIG. 12


TRACKING ERROR
25
LPF
20
PHASE DIFFERENCE CALCULATION / GATE UNIT
31
GATE
23
PHASE DIFFERENCE DETECTOR
40
DETERMINATION UNIT
75
AND
74a
DISCRIMI-NATION
74b
DISCRIMI-NATION
Ra+Rd
Rb+Rc
21
22
Ra
Rb
Rd
Rc
QUADRUPLE DETECTOR
A
B
C
D
11
TANGENTIAL DIRECTION OF TRACK

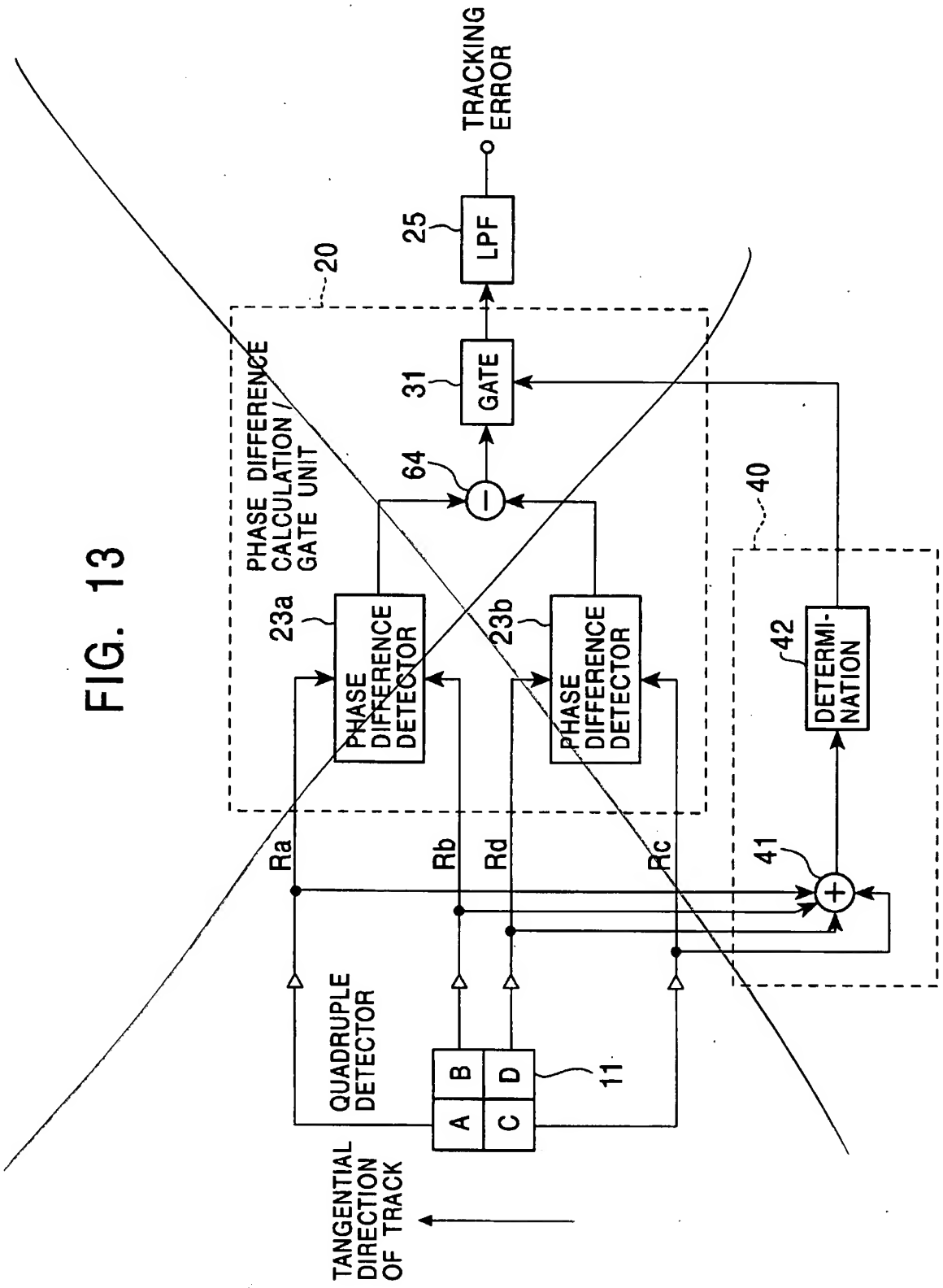


	Type	L #	Hits	Search Text	DBs	Time Stam	Comment	Error Definit	Er
7	BRS	L7	713	369/47.28	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	2007/11/16 16:07			
8	BRS	L8	24827	G11B005/09	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	2007/11/16 16:07			
9	BRS	L9	1928	331/16	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	2007/11/16 16:07			
10	BRS	L10	3802	331/17	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	2007/11/16 16:07			
11	BRS	L11	10324	H03L007/00	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	2007/11/16 16:08			
12	BRS	L13	8753	H03L007/06	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	2007/11/16 16:08			
13	BRS	L12	1573	327/159	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	2007/11/16 16:09			
14	BRS	L14	16	327/159	US-PGPUB	2007/11/16 16:09			
15	BRS	L15	46	331/16	US-PGPUB	2007/11/16 16:09			
16	BRS	L16	100	331/17	US-PGPUB	2007/11/16 16:09			
17	BRS	L17	48	369/47.28	US-PGPUB	2007/11/16 16:09			

Hits Details HTML

Ready NUM

FIG. 13


TRACKING ERROR
25
LPF
20
31
GATE
64
PHASE DIFFERENCE CALCULATION / GATE UNIT
40
23a
PHASE DIFFERENCE DETECTOR
23b
PHASE DIFFERENCE DETECTOR
42
DETERMI-NATION
Ra
Rb
Rd
Rc
41
QUADRUPLE DETECTOR
A
B
C
D
11
TANGENTIAL DIRECTION OF TRACK

EAST - [default.wsp:1]

File View Edit Tools Window Help

- Drafts
- Pending
- Active
  - L1: (11136) (pulse time) adj5 (width near8 detect$4)
  - L2: (234249) (phase near8 shift$4)
  - L3: (1064) 1 and 2
  - L4: (17402) (count$3 near8 (frequency adj5 divid$3))
  - L5: (106) 3 and 4
- Failed
- Saved
  - S1: (5) "7085204"
- Favorites
- Tagged (0)

Search List Browse Queue Clear

DBs USPAT

Default operator: OR

Plurals

Highlight all hit terms initially

BRS form IS&R form Image Text HTML

	Type	L #	Hits	Search Text	DBs	Time Stam	Comment	Error	Definit	Er
1	BRS	L1	11136	(pulse time) adj5 (width near8 detect$4	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO	2007/11/15 12:44				
2	BRS	L2	234249	(phase near8 shift$4)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO	2007/11/15 12:45				
3	BRS	L3	1064	1 and 2	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO	2007/11/15 12:45				
4	BRS	L4	17402	(count$3 near8 (frequency adj5 divid	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO	2007/11/15 12:46				
5	BRS	L5	106	3 and 4	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO	2007/11/15 12:46				

Hits Details HTML

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EAST - [default.wsp:1]

File View Edit Tools Window Help

Pending
Active
- L1: (11136) (pulse time) adj5 (width near8 detect$4)
- L2: (234249) (phase near8 shift$4)
- L3: (1064) 1 and 2
- L4: (17402) (count$3 near8 (frequency adj5 divid$3))
- L5: (106) 3 and 4
- L6: (125826) (pll (phase adj5 loop))
- L7: (1239) 1 and 6
- L8: (127) 4 and 7
- L9: (82) 2 and 8
- L10: (317) 6 same 1
- L11: (20) 4 and 10

Search | Edit | Browse | Queries | Clear

DBs: US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB

Default operator: OR

Plurals

Highlight all hit terms initially

4 and 10

BRS form | IS&R form | Image | Text | HTML

	U	Document ID	Issue Dat	Page	Title	Current OR	Current XR	Retrieval	Inventor	S	C	P
5		US 7180377 B1	20070220	15	Method and apparatus for a hybrid phase lock	331/17	327/157; 331/DIG.2		Leong; Colin Wai Mun et al.			
6		US 7177611 B2	20070213	16	Hybrid control of phase locked loops	455/260	331/17; 375/376;		Goldman; Stanley Jay			
7		US 7084709 B1	20060801	15	Hybrid analog/digital phase lock loop frequen	331/11	327/156; 327/157;		Leong; Colin Wai Mun et al.			
8		US 6580775 B1	20030617	6	Method of detecting frequency of digital ph	375/375	327/151; 327/162;		Park; Hyun-Soo et al.			
9		US 6518845 B2	20030211	13	PLL frequency synthesizer circuit	331/16	331/DIG.2		Nakamichi; Hiroto			
10		US 6441691 B1	20020827	16	PLL cycle slip compensation	331/25	327/12; 331/156		Jones; Theron et al.			
11		US 6392641 B1	20020521	23	PLL circuit for digital display apparatus	345/213	331/1A; 331/20;		Nishimura; Eizo et al.			
12		US 5783972 A	19980721	14	Power saving PLL circuit	331/17	327/157; 327/159;		Nishikawa; Masato			
13		US 5661425 A	19970826	24	Digital PLL circuit	327/159	327/147; 327/150;		Minoda; Hidenori et al.			
14		US 5548249 A	19960820	34	Clock generator and method for generating a	331/1A	327/107; 327/150;		Sumita; Masaya et al.			
15		US 4996596 A	19910226	28	Phase synchronizing circuit in video signal	348/542	348/E5.021		Hirao; Yoshichika et al.			
16		US 4727530 A	19880223	29	Disc rotation control device for a disc playe	369/47.45	369/111; 386/86		Tomisawa; Norio			
17		US 4707856 A	19871117	12	AM stereo receiver	381/15	324/76.47; 324/76.55		Tanaka; Kanji et al.			
18		US 4688246 A	19870818	14	CATV scrambling system with compressed digital	380/237	348/E7.028 ;		Eilers; Carl G. et al.			
19		US 3868677 A	19750225	12	PHASE-LOCKED VOLTAGE-TO-DIGITAL CONV	341/157	341/158		Kidd; Marshall C.			
20		GB 2194714 A	19880309	11	Phase locked loop for ISDN in telephone syste				ERKKU, J H et al.			

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